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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10050417	FILING DATE 01/16/2002	CLASS 438	SUBCLASS 311	GAU 2842	EXAMINER [Signature]
<b>**APPLICANTS:</b> Phan Khoi, Ernardt Jeffrey, Cheng Jerry, Bartlett Richard, Coniglio Anthony, Grundke Wolfram, Bradway Carol, Sutton Daniel, Mazur Martin.					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLN CLAIMS BENEFIT OF 60/285 197 04/20/2001					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no verified and Acknowledged Examiners initials				ATTORNEY DOCKET NO G0131	
TITLE : Methods and systems for controlling resist residue defects at gate layer in a semiconductor device manufacturing process					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Assistant Examiner	
		Primary Examiner	
<input type="checkbox"/> <b>PREPARED FOR ISSUE</b>		Application Examiner	
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